

**Listing of the Claims:**

The following is a complete listing of all the claims in the application, with an indication of the status of each:

1-5. Canceled

6. (Currently amended) A device that detects an electronic watermark from a compressed embedded in an original image, comprising:

a table file defining an instruction corresponding to bit-data included in said electronic watermark;

~~a circuit reading a said compressed original image data and a table date, said table data defining an instruction corresponding to bit-data included in a part of an electronic watermark;~~

a circuit decoding the said compressed original image data to produce a decoded data in which the watermark is embedded;

~~a circuit performing inverse discrete cosine transform (IDCT) for the said decoded data;~~

~~a circuit detecting electronic watermark data embedded in the data for which IDCT has been performed along with said bit-data; and~~

a circuit performing a processing according to said instruction corresponding to said bit-data.

7. (Currently amended) The device according to claim 6 wherein the electronic watermark data is eight-bit data and the said bit-data is four-bit data.

8. (Currently amended) The device according to claim 6 wherein characters are displayed according to the said instruction corresponding to said bit-data.

9. (Currently amended) The device according to claim 6 wherein a web site on the Internet is accessed according to the said instruction corresponding to said bit-data.

10. (Currently amended) The device according to claim 6 wherein an application

program is started according to the said instruction corresponding to said bit-data.

11-15. Canceled

16. (Currently amended) A method for detecting an electronic watermark embedded in an original image, comprising the steps of:

reading a compressed image data and a table data, said table data defining an instruction corresponding to bit-data included in a part of an electronic watermark;

decoding said compressed image data in which the watermark is embedded;

performing inverse discrete cosine transform (IDCT) for the decoded data obtained from said decoding step;

detecting electronic watermark data embedded in the data for which IDCT has been performed; and

performing processing according to said instruction.

17. (Currently amended) The method according to claim 16 wherein the electronic watermark is eight-bit data and the said bit-data is four-bit data.

18. (Currently amended) The method according to claim 16 wherein characters are displayed according to said the instruction.

19. (Currently amended) The method according to claim 16 wherein a web site on the Internet is accessed according to said the instruction.

20. (Currently amended) The method according to claim 16 wherein an application program is started according to said the instruction.

21. Canceled

22. (Currently amended ) A computer-readable recording medium storing therein a program for detecting an electronic watermark embedded in an original image,

said program causing a computer to:

read a compressed image data and a table data, said table data defining an instruction corresponding to bit-data included in a part of an electronic watermark;

decode the said compressed image data in which the said electronic watermark is embedded to obtain decoded data;

perform inverse discrete cosine transform (IDCT) for the decoded data;

detect electronic watermark data embedded in the data for which IDCT has been performed; and

perform processing according to said instruction.

23. (Currently amended) A device that detects an electronic watermark from ~~embedded in~~ an original image, comprising:

a table file defining an instruction corresponding to bit-data included in said electronic watermark;

~~a circuit reading an said original image data and a table data, said table data defining an instruction corresponding to bit-data included in a part of an electronic watermark;~~

~~a circuit detecting said electronic watermark embedded in from said original image imaged data along with said bit-data; and~~

~~a circuit performing and processing according to based on said instruction corresponding to said bit-data.~~